

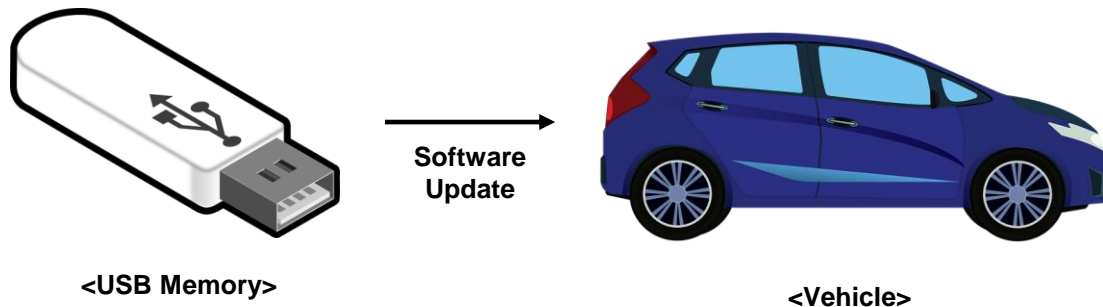
Software Update System - Summary

Summary:

- ❑ Software Update System using USB Memory
- ❑ USB Host 동작
- ❑ SPI 전송 속도 1MHz
- ❑ USB 파일 및 인식 에러 발생 시 보고

Part :

- ❑ **PIC32MM0128G**
 - ❑ 650 nA Sleep Current for RAM Retention mode and 5 μ A for Regulator Standby mode
 - ❑ microMIPS™ Instruction Set for 35% Smaller Code and 98% Performance compared to MIPS32 Instructions
 - ❑ USB 2.0 Compliant Full-Speed and Low-Speed Device, Host and On-The-Go (OTG) Controller with a dedicated DMA
 - ❑ USB Device mode operation from FRC oscillator with no external crystal oscillator required
 - ❑ 4-Channel Hardware DMA with Automatic Data Size Detection and CRC Engine
- ❑ **MCP1755**
 - ❑ High PSRR: >70 dB @ 1 kHz, typical
 - ❑ 68.0 μ A Typical Quiescent Current
 - ❑ Input Operating Voltage Range: 3.6V to 16.0V
 - ❑ 300 mA Output Current for all Output Voltages
 - ❑ Low Dropout Voltage, 300 mV typical @ 300 mA



Software Update System - Block Diagram

