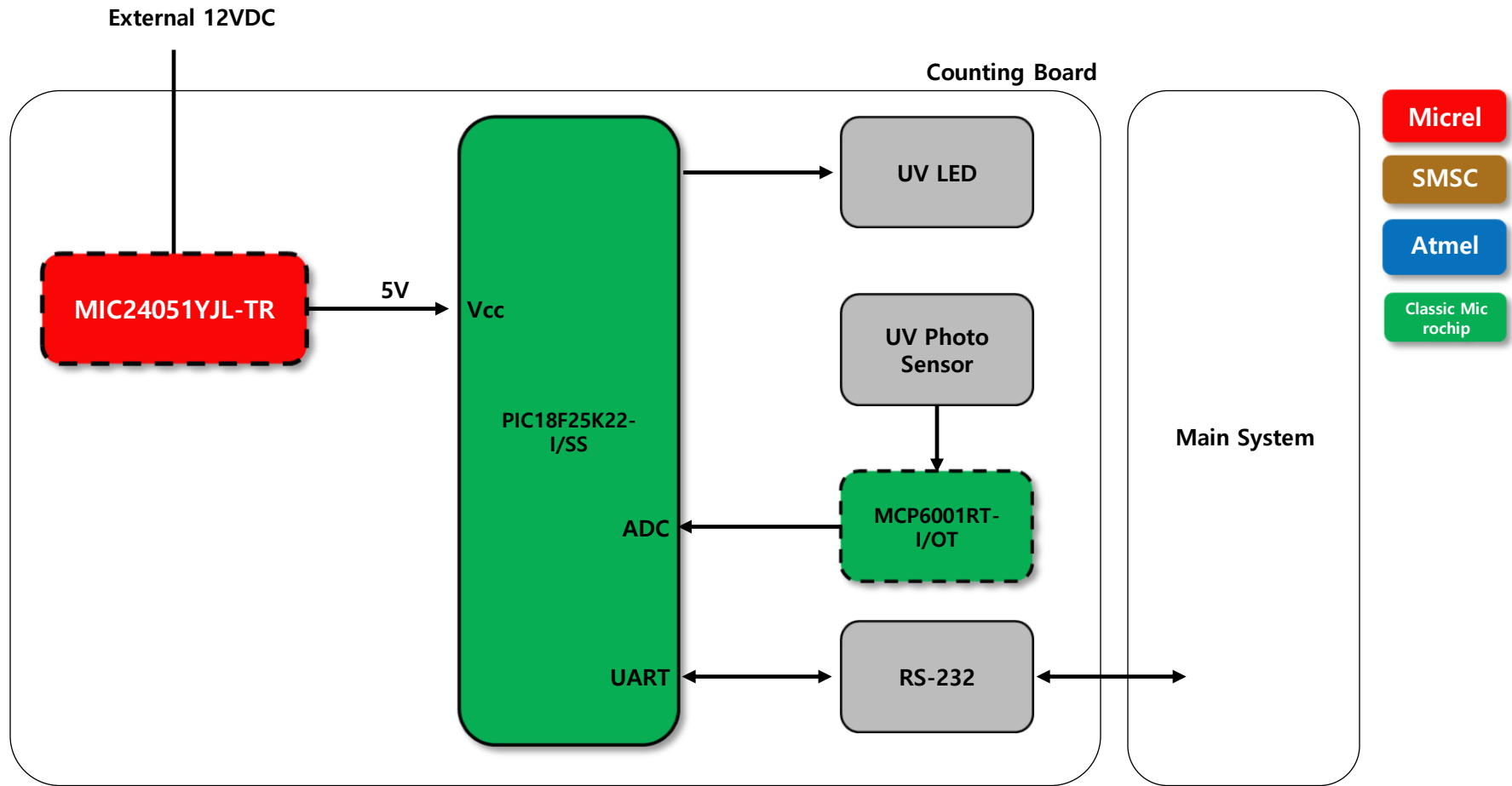
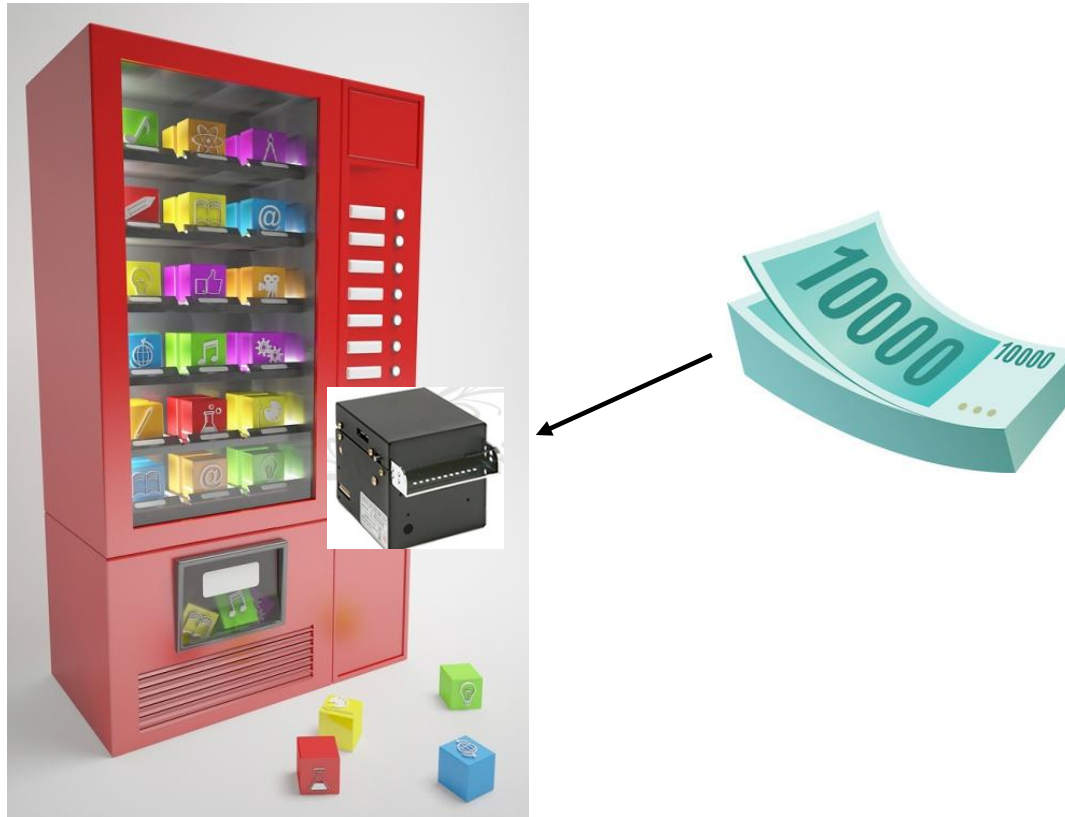


Bill Counting Machine – Block Diagram



Bill Counting Machine – System



□ Microchip MCU, PIC18F25K22

- 8-bit Processor
- Up to 64MHz, 32KB Flash, 1.5KB SRAM, 256bytes EEPROM
- up to 16 MIPS operation
- 16-bit wide instructions, 8-bit wide data path
- Configurable Custom Logic (CCL)
- Extreme Low-Power (XLP) : Sleep mode: 100 nA, typical

□ Microchip DC-DC Buck, MIC24051

- Hyper Speed Control™ architecture enables
- High delta V operation ($V_{IN} = 19V$ and $V_{OUT} = 0.8V$)
- Small output capacitance
- 4.5V to 19V input voltage
- Adjustable output from 0.8V to 5.5V ($\pm 1\%$ accuracy)
- Any Capacitor™ stable & Zero-to-high ESR

□ Microchip Op Amp, MCP6001

- Gain Bandwidth Product (GBWP): 1 MHz
- Quiescent Current: 100 μA (typ.)
- Supply voltage range: 1.8V to 6V
- Rail to Rail Input and Output
- Extended Temperature Range
- Space Saving Package Offering: SC70, SOT23